

IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph on page 11, between lines 16-18 of the specification with the following:

It is possible with the second circuit arrangement to realize also other interlacing schemes, for example ~~a~~an "abcdabcd" interlaced operation, if further clock bus lines A and B are provided and are connected, for example, to the converters Um3 and Um4.

Replace the paragraph on page 12, between lines 3-5 of the specification with the following:

A positive or negative supply voltage is applied to the inverters again via two DC buses (+, -). Ten bus lines are thus necessary in total ~~independently~~independent of the number of rows and columns of the display.